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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant(s) : Zheng et al.  
Serial No. : 10/790,492  
Filed : March 1, 2004  
Title : ATOMIC LAYER DEPOSITION OF CAPACITOR DIELECTRIC  
Docket : MIO 0082 N2/40509.292  
Art Unit : 2822

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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James E. Beyer

Reg. No. 39,564

Sir:

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UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

Applicants submit herewith patents, publications, and other information of which they are aware, which they believe may be material, as defined in 37 CFR §1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56(a). While the information referred to in this Information Disclosure Statement may be material pursuant to 37 CFR §1.56(b), the filing of this Information Disclosure Statement is not intended to, pursuant to 37 CFR §1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103, and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish otherwise. Further, pursuant to 37 CFR §1.97(g), the filing of this Statement should not be construed as a statement that a search has been made or that no other material information exists.


Serial No. 10/790,492  
Docket MIO 0082 N2/40509.292

This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is believed to be filed before the mailing date of a first office action on the merits.

The above application is a continuation of U.S. Application Serial No. 10/228,911 filed August 27, 2002. Pursuant to §1.98(d), copies of those items cited by or submitted to the Office in the prior application are not being submitted.

The Office has waived the requirement pursuant to 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. Therefore, no copies of each cited U.S. patent and each cited U.S. patent application publication are enclosed, but the cited U.S. patents and the cited U.S. patent application publications are listed on PTO/SB/08A.

Respectfully submitted,  
DINSMORE & SHOHL LLP

By   
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/790,492
Filing Date	March 1, 2004
First Named Inventor	Lingyi A. Zheng
Art Unit	2822
Examiner Name	
Attorney Docket Number	MIO 0082 N2/40509.292

Sheet 1 of 2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 5,023,683	06/11/1991	Yamada	
		US- 5,624,865	04/29/1997	Schuegraf et al.	
		US- 5,905,280	05/18/1999	Liu et al.	
		US- 5,960,297	09/28/1999	Saki	
		US- 6,042,652	03/28/2000	Hyun et al.	
		US- 6,175,129	01/16/2001	Liu et al.	
		US- 6,207,494	03/27/2001	Graimann et al.	
		US- 2001/0024387 A1	09/27/2001	Raaijmakers et al.	
		US- 2002/0047151 A1	04/25/2002	Kim et al.	
		US- 2002/0068466 A1	06/06/2002	Lee et al.	
		US- 6,605,4549 B2	08/12/2003	Leu et al.	
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**FOREIGN PATENT DOCUMENTS**

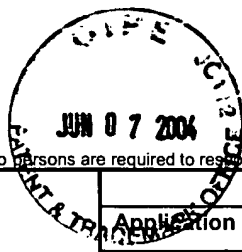
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		KR 2002002722	06/30/2000	Yoo		
		KR 2002050364	12/21/2000	Cho		

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Sheet	2	of	2
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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		0.13 um CMOS DEVICE CHARACTERISTICS, <a href="http://www.semiconductor.net/semiconductor/issues/issues/2000/200001/images/Six0001">http://www.semiconductor.net/semiconductor/issues/issues/2000/200001/images/Six0001</a> , July 31, 2001, p. 1	
		PETER SINGER, Atomic Layer Deposition Targets Thin Films, Semiconductor International-September 1999, Technology News, p. 1,2	
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		GENUS, What is ALD? p. 1	
		ALD Products, ALD Products GENUS, About Genus/investor Relations/News/Careers/Support & Training/ ALD Products/CVD Products/Markets/Site Index/Glossary/Home/Legal Notices/Copyright 2001 Genus Inc., p. 1	

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